PRODUCT   :   LCD MODULE
MODEL NO   :   LCD096LAB-02-100N
SUPPLIER   :   LCD Mikroelektronik GmbH
DATE   :   Oct.12.2019

SPECIFICATION

Prepared by	Checked	Approved

CUSTOMER:
MODEL NO.:   DATE:

Approved	Checked	Department

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Revision Record

Rev No.	Rev Date	Contents	Remarks
1.0	2019.03.27	New creation	
1.1	2019.10.12	Modify pin assignment	P6

1. General Specifications

No	Item	Contents	Unit
1	Size	0.96	inch
2	Resolution	80RGB*160	
3	Interface	4-SPI	
4	Color Depth	262K	
5	Pixel Pitch	0.135*0.135	mm
6	Display Mode	Transmissive，IPS,NB	
7	Viewing Direction	ALL VIEW	
8	LCM(W x H x D)	13.8*28*1.9	mm
9	Active Area (W x H)	10.8*21.696	mm
10	With/Without TSP	Without TSP	
11	LED Numbers	1	

2. Mechanical Drawing
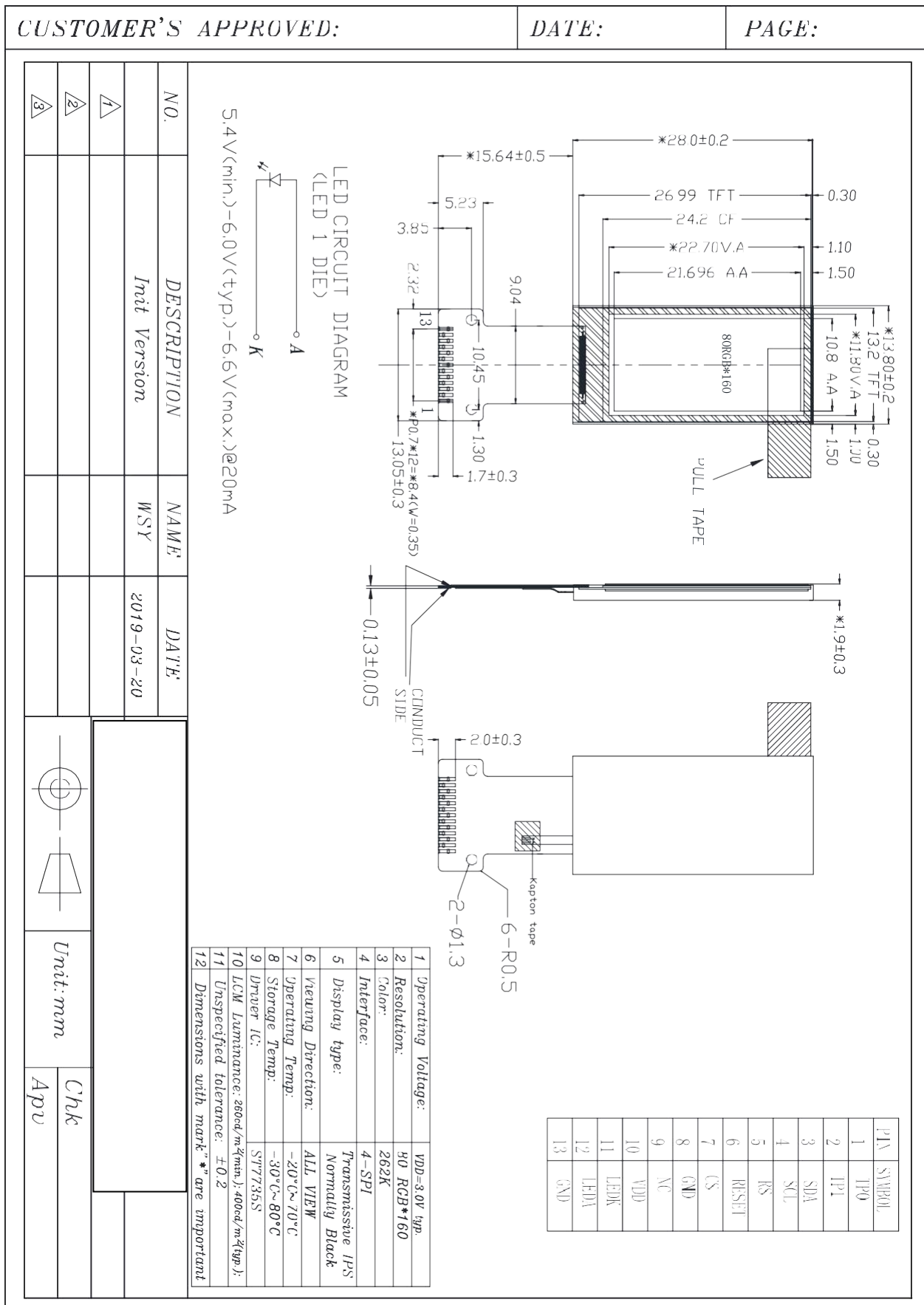
3. PIN Assignment

Pin No	Symbol	I/O	Function	Remark
1	TP0	-	No connection	
2	TP1	-	No connection	
3	SDA	I/O	The serial input/output signal in serial interface mode	
4	SCL	I	Display data/command Selection Pin in MCU Interface. D/CX='1': Display Data or Parameter. D/CX='0': Command Data. In Serial Interface, this is used as SCL. If not used, please fix this pin at VDDI or DGND level.	
5	RS	I	This pin is used as D/CX (data/ command selection)..	
6	RESET	I	This signal will reset the device and it must be applied to properlyinitialize the chip. Signal is active low	
7	CS	I	Chip Selection Pin Low Enable.	
8	GND	P	Ground	
9	NC	-	No connection	
10	VDD	P	Power supply	
11	LEDK	P	LED cathode	
12	LEDA	P	LED anode	
13	GND	P	Ground	

Ps. For further details, please refer to ST7735Sdata sheet.

4. Absolute Maximum Rating

AGND = GND = 0V , Ta = 25° C

Item	Symbol	Min	Max	Unit	Remark
Power Voltage	VDD	-0.3	4.8	V	
Operating Temperature	T_{OPR}	-20	70	° C	
Storage Temperature	T_{STG}	-30	80	° C	

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

5. Electrical Characteristics

5.1. Recommended Operating Condition

AGND = GND = 0V , Ta = 25° C

Item	Symbol	Min	Typ.	Max	Unit	Remark
Power Voltage	VDD	2.3	3.0	3.7	V	
Input logic high voltage	V_{IH}	0.7 IOVCC	-	IOVCC	V	
Input logic low voltage	V_{IL}	0	-	0.3IOVCC	V	

5.2. Recommended Driving Condition for Backlight

Ta = 25° C

Item	Symbol	Min	Typ.	Max	Unit	Remark
Forward Voltage	Vf	5.4	6.0	6.6	V	
Forward Current	If		20		mA	
Operating Life Time	-	20000			Hours	

Note 1:The LED supply voltage is defined by the number of LED at Ta=25℃ and If=20mA.

Note 2:The "Operating Life Time" is defined as the module brightness decrease to 50% original brightness at Ta=25℃ and If= 20mA. The LED lifetime could be decreased if operating If is larger than 20mA.

6. Timing Characteristics

6.1. DC Electrical Characteristics

VDD

| Parameter	Symbol	Condition	Specification			Unit	Related Pins
			Min	Typ	Max		
Power & Operation Voltage							
System Voltage	VDD	Operating Voltage	2.3	3.0	4.8	V	
Interface Operation Voltage	VDDI	I/O Supply Voltage	1.65	1.8	3.7	V	
Gate Driver High Voltage	VGH		10		15	V	
Gate Driver Low Voltage	VGL		-12.4		-7.5	V	
Gate Driver Supply Voltage		| VGH-VGL |	17.5		27.5	V	
Input / Output							
Logic-High Input Voltage	VIH		0.7VDDI		VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.3VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS		0.2VDDI	V	Note 1
Logic-High Input Current	IIH	VIN = VDDI			1	uA	Note 1
Logic-Low Input Current	IIL	VIN = VSS	-1			uA	Note 1
Input Leakage Current	IIL	IOH = -1.0mA	-0.1		+0.1	uA	Note 1
VCOM Voltage							
VCOM Amplitude	VCOM		-2		-0.425	V	
Source driver							
Source Output Range	Vsout		0.1		GVDD	V	
Gamma Reference Voltage	GVDD		3.15		4.7	V	
Source Output Settling Time	Tr	Below with 99% precision			20	us	Note 2
Output Offset Voltage	Voffset				35	mV	Note 3

Notes:

1. TA= -30 to 85℃.

2. Source channel loading= 2KΩ+12pF/channel, Gate channel loading=5KΩ+40pF/channel.

3. The Max. value is between measured point of source output and gamma setting value.

6.2. Timing

Parallel Interface Characteristics: 18, 16, 9 or 8-bit Bus (8080 Series MCU Interface)
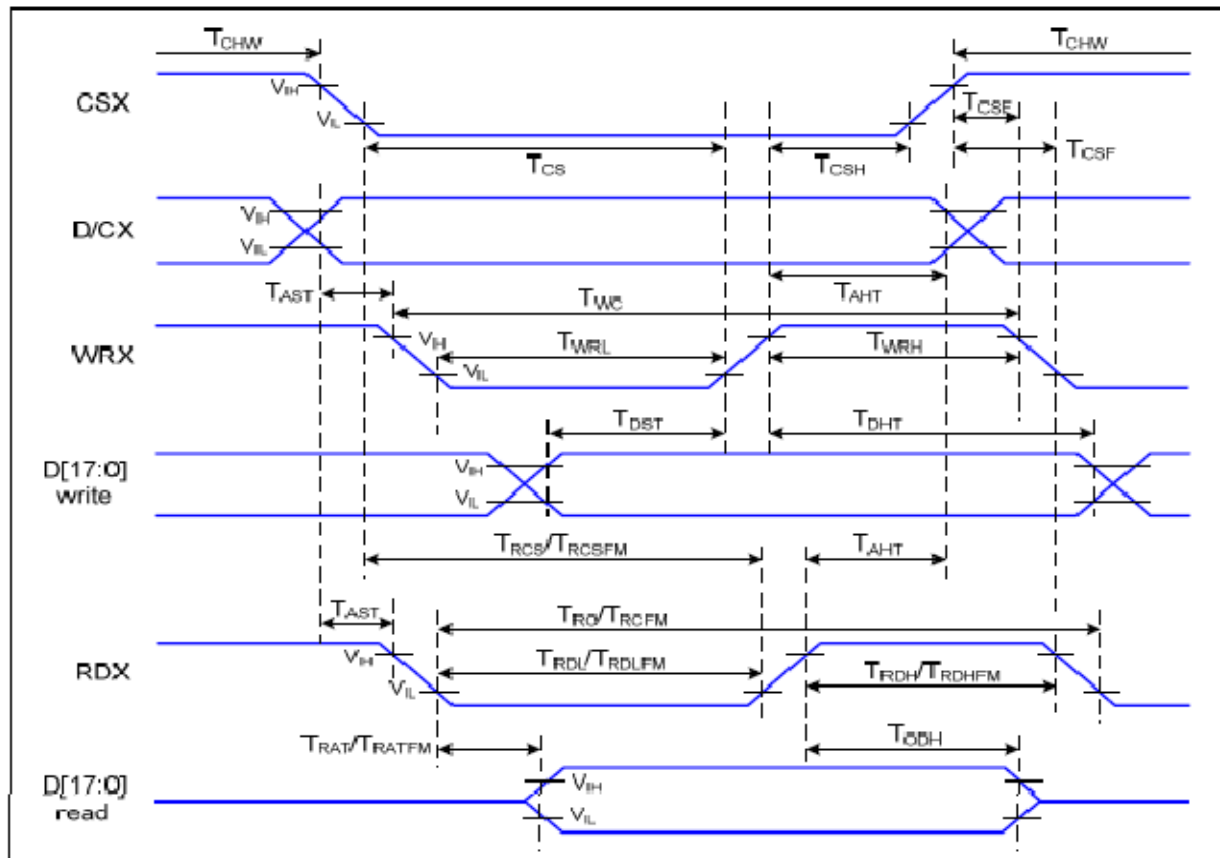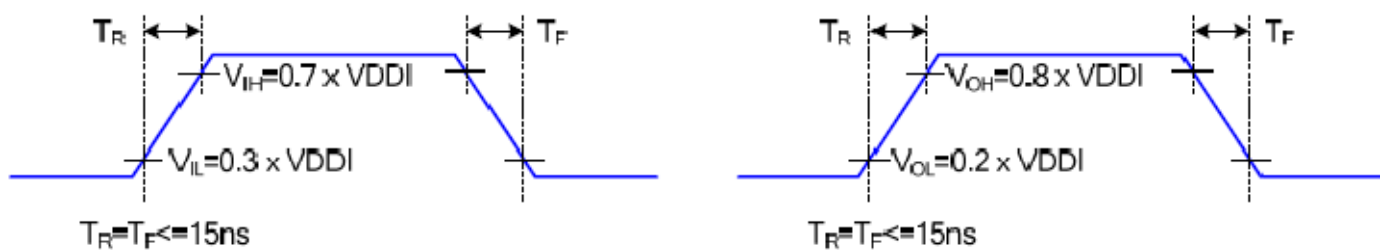

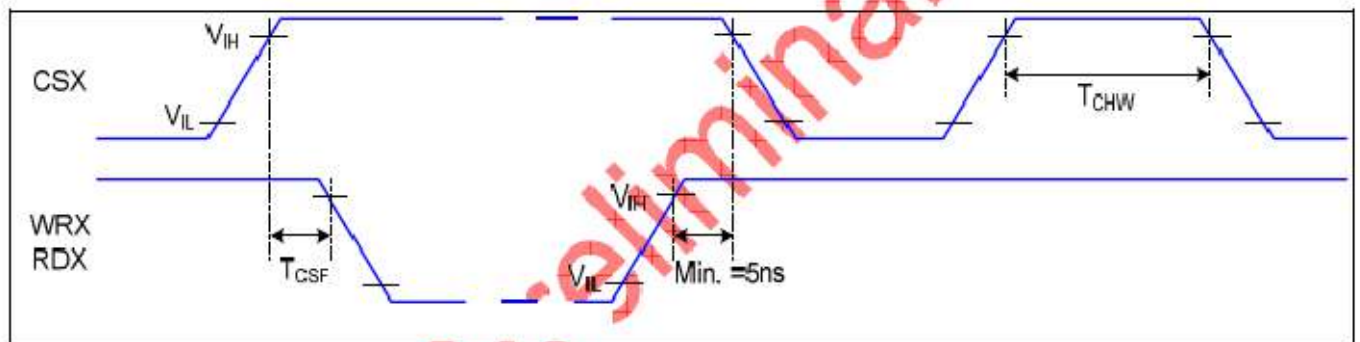Parallel Interface Timing Characteristics (8080 Series MCU Interface)

Ta=25 ℃, VDDI=1.65~3.7V, VDD=2.3~4.8V

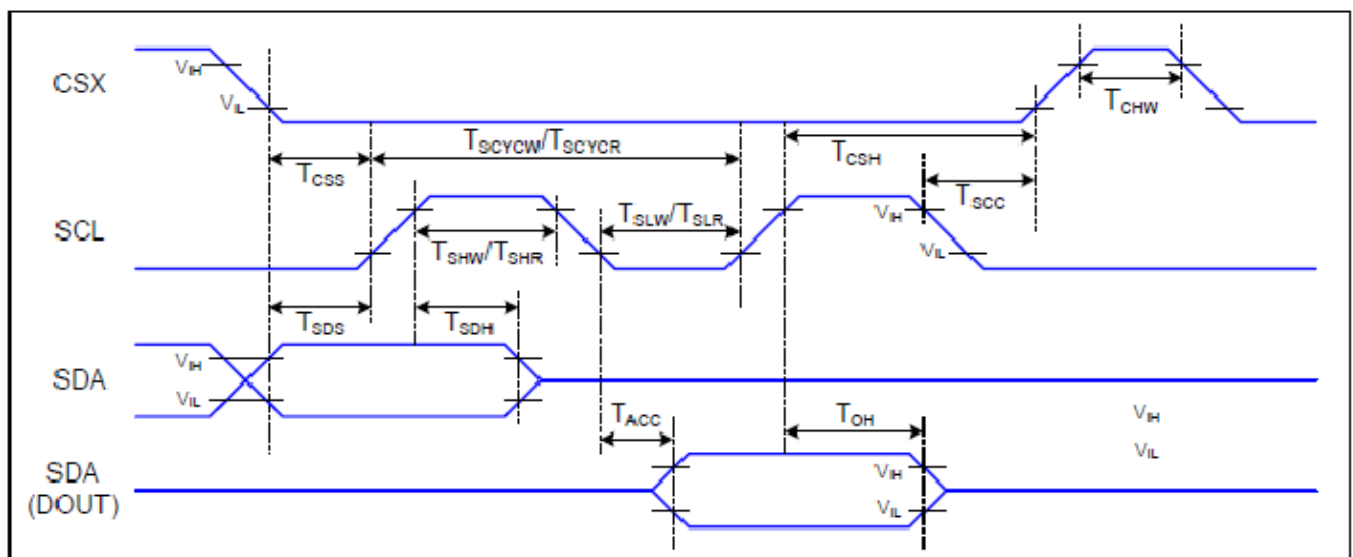| Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	TAST	Address Setup Ttime	TBD		ns	-
	TAHT	Address Hold Time (Write/Read)	TBD		ns	
CSX	TCHW	Chip Select "H" Pulse Width	TBD		ns	
	TCS	Chip Select Setup Time (Write)	TBD		ns	
	TRCS	Chip Select Setup Time (Read ID)	TBD		ns	
	TRCSFM	Chip Select Setup time (Read FM)	TBD		ns	
	TCSF	Chip Select Wait Time (Write/Read)	TBD		ns	
	TCSH	Chip Select Hold Time	TBD		ns	
WRX	TWC	Write Cycle	TBD		ns	
	TWRH	Control Pulse "H" Duration	TBD		ns	
	TWRL	Control Pulse "L" Duration	TBD		ns	
RDX (ID)	TRC	Read Cycle (ID)	TBD		ns	When Read ID Data
	TRDH	Control Pulse "H" Duration (ID)	TBD		ns	
	TRDL	Control Pulse "L" Duration (ID)	TBD		ns	

RDX (FM)	TRCFM	Read Cycle (FM)	TBD		ns	When Read from Frame Memory
	TRDHFM	Control Pulse "H" Duration (FM)	TBD		ns	
	TRDLFM	Control Pulse "L" Duration (FM)	TBD		ns	
D[17:0]	TDST	Data Setup Time	TBD		ns	For CL=30pF
	TDHT	Data Hold Time	TBD		ns	
	TRAT	Read Access Time (ID)		TBD	ns	
	TRATFM	Read Access Time (FM)		TBD	ns	
	TODH	Output Disable Time	TBD	TBD	ns	

Rising And Falling Timing for Input And Output Signal

Chip Selection (CSX) Timing
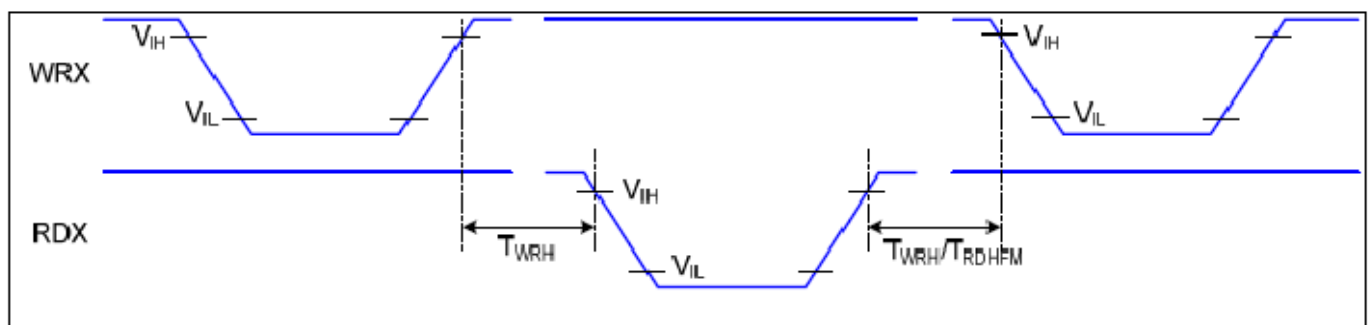
Write-to-Read And Read-to-Write Timing

Parallel Interface Characteristics: 18, 16, 9 or 8-bit Bus (6800 Series MCU Interface)
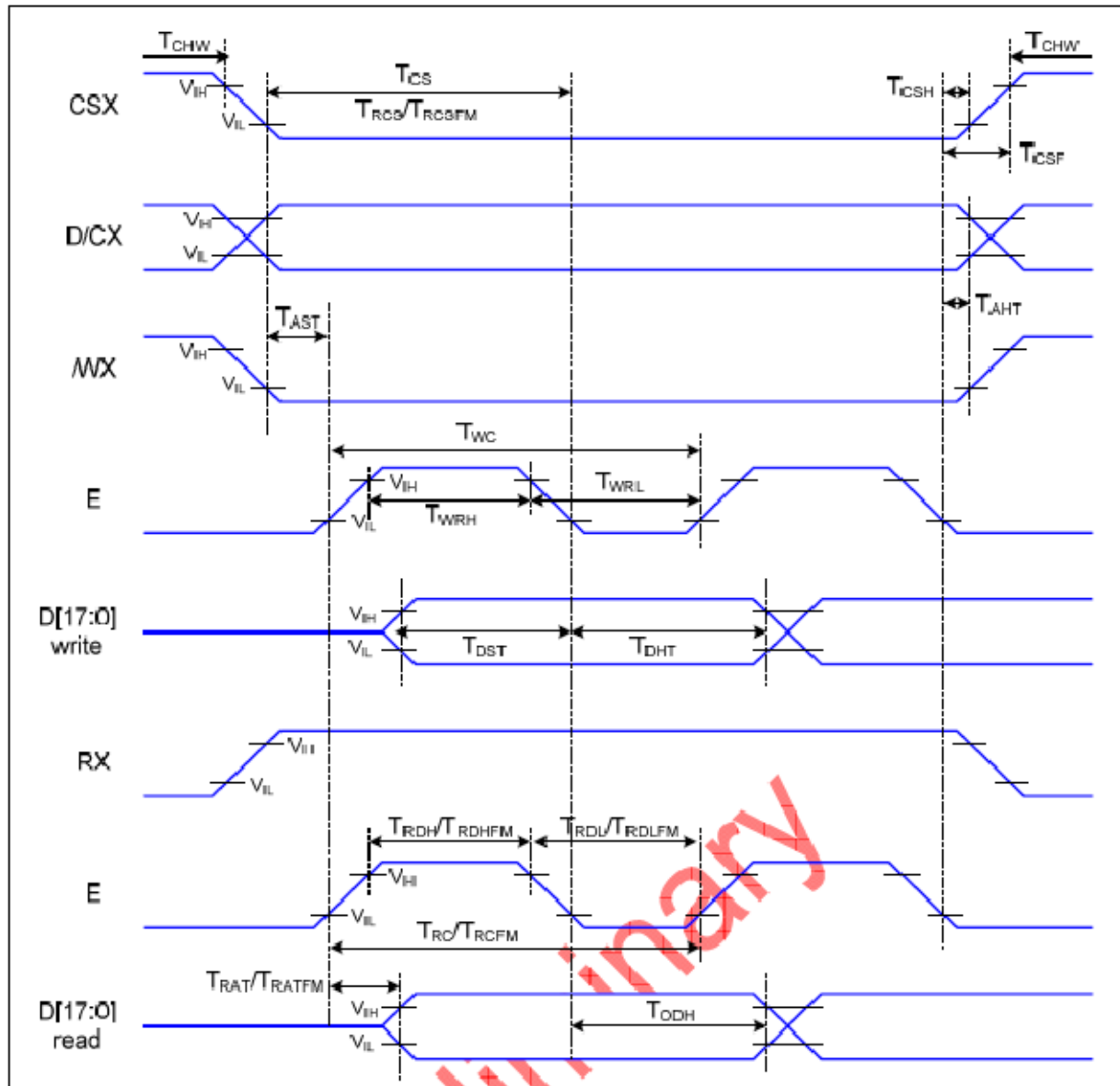
Ta=25 ℃, VDDI=1.65~3.7V, VDD=2.3~4.8V

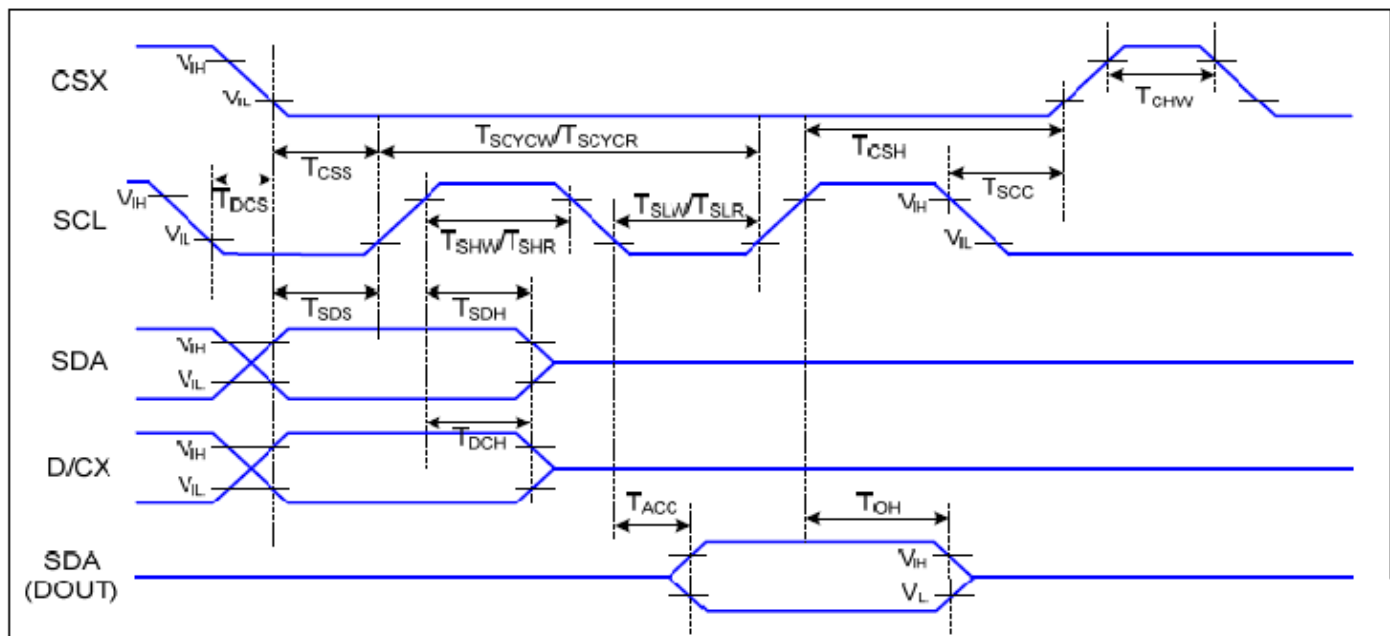| Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T_{AST}	Address Setup Time	TBD		ns	-
	T_{AHT}	Address Hold Time (Write/Read)	TBD		ns	
CSX	T_{CHW}	Chip Select "H" Pulse Width	TBD		ns	-
	T_{CS}	Chip Select Setup Time (Write)	TBD		ns	
	T_{RCS}	Chip Select Setup Time (Read ID)	TBD		ns	
	T_{RCSFM}	Chip Select Setup Time (Read FM)	TBD		ns	
	T_{CSF}	Chip Select wait Time (Write/Read)	TBD		ns	
	T_{CSH}	Chip Select Hold Time	TBD		ns	
WRX	T_{WC}	Write Cycle	TBD		ns	
	T_{WRH}	Control Pulse "H" Duration	TBD		ns	
	T_{WRL}	Control Pulse "L" Duration	TBD		ns	
RDX (ID)	T_{RC}	Read Cycle (ID)	TBD		ns	When Read ID Data
	T_{RDH}	Control Pulse "H" Duration (ID)	TBD		ns	
	T_{RDL}	Control Pulse "L" Duration (ID)	TBD		ns	
RDX (FM)	T_{RCFM}	Read Cycle (FM)	TBD		ns	When Read From Frame Memory
	T_{RDHFM}	Control Pulse "H" Duration (FM)	TBD		ns	
	T_{RDLFM}	Control Pulse "L" Duration (FM)	TBD		ns	
D[17:0]	T_{DST}	Data Setup Time	TBD		ns	For Maximum CL=30pF
	T_{DHT}	Data Hold Time	TBD		ns	
	T_{ODH}	Output Disable Time	TBD	TBD	ns	For Minimum CL=8pF

Serial Interface Characteristics (3-line Serial)

Ta=25 ℃, VDDI=1.65~3.7V, VDD=2.3~4.8V

Signal	Symbol	Parameter	Min	Max	Unit	Description
CSX	TCSS	Chip Select Setup Time (Write)	TBD		ns	
	TCSH	Chip Select Hold Time (Write)	TBD		ns	
	TCSS	Chip Select Setup Time (Read)	TBD		ns	
	TSCC	Chip Select Hold Time (Read)	TBD		ns	
	TCHW	Chip Select "H" pulse width	TBD		ns	
SCL	TSCYCW	Serial Clock Cycle (Write)	TBD		ns	
	TSHW	SCL "H" Pulse Width (Write)	TBD		ns	
	TSLW	SCL "L" Pulse Width (Write)	TBD		ns	
	TSCYCR	Serial Clock Cycle (Read)	TBD		ns	
	TSHR	SCL "H" Pulse Width (Read)	TBD		ns	
	TSLR	SCL "L" Pulse Width (Read)	TBD		ns	
SDA (DIN) (DOUT)	TSDS	Data Setup Time	TBD		ns	For Maximum CL=30pF
	TSDH	Data Hold Time	TBD		ns	For Minimum CL=8pF
	TACC	Access Time	TBD	TBD	ns	
	TOH	Output Disable Time	TBD	TBD	ns	

Serial Interface Characteristics (4-line Serial)

Ta=25·c, VD DI =1 65 3 7V, VDD=2 3 4 BV

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	TCSS	Chip Seled Setup Time (Write)	TB	)	ns	
	TCSH	Chip Select HcM T ime (Write)	1r-t	J·	ns	
	TCSS	Chip Seled Setup Time (Read)		JY	n s	
	TSCC	Chip Select HcM T ime (eapp,l		;;-im	ns	
	TCHW	Chip Seled "IH" Pulse	TBD		ns	
SCL	TSCYCW	Serial Clo ck Cyd	TBD		n s	-Wriite Command & Data Ram
	TSHW	SCL "H" Pu!w (W e)	TBD		ns	
	TSLW	SCL "L" Pu f dth(W riie)	TBD		ns	
	TSC YC R	Serial Clock Cyff e (Read)	TBD		ns	-Read Command & Data Ram
	TSHR	SCL" H" P u lse Wii dt h (Rea d)	TBD		ns	
	TSLR	SCL "L" Pull se Width ,(Read)	TBD		ns	
D/CX	TDCS	D/CX Se tlup Time	TBD		ns	
	TD CH	0 /CX Ho ld Tiim e	TBD		ns	
SDA (DI NI) (DOUT)	TSDS	Data Setup Time	TBD		ns	For Maxii m u m CL=30pF For Minimum CL=BpF
	TSDH	Data Ho ld Tii me	TBD		riS	
	TACC	Access Time	TBD	TBD	ns	
	TOIH	Output Di sable Tii m e	TBD	TBD	ns	

6.3. Power ON/OFF Sequence

VDDI and VDD can be applied in any order

VDD and VDDI can be powered down in any order

During power off, if LCD is in the Sleep Out mode, VDD and VDDI must be powered down minimum 120msec after RESX has been released.

During power off, if LCD is in the Sleep In mode, VDDI or VDD can be powered down minimum 0msec after RESX has been released.

CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

Note 1: There will be no damage to the display module if the power sequences are not met.
Note 2: There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.
Note 3: There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.
Note 4: If RESX line is not held stable by host during Power On Sequence as defined in the sequence below, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is not guaranteed.

The power on/off sequence is illustrated below
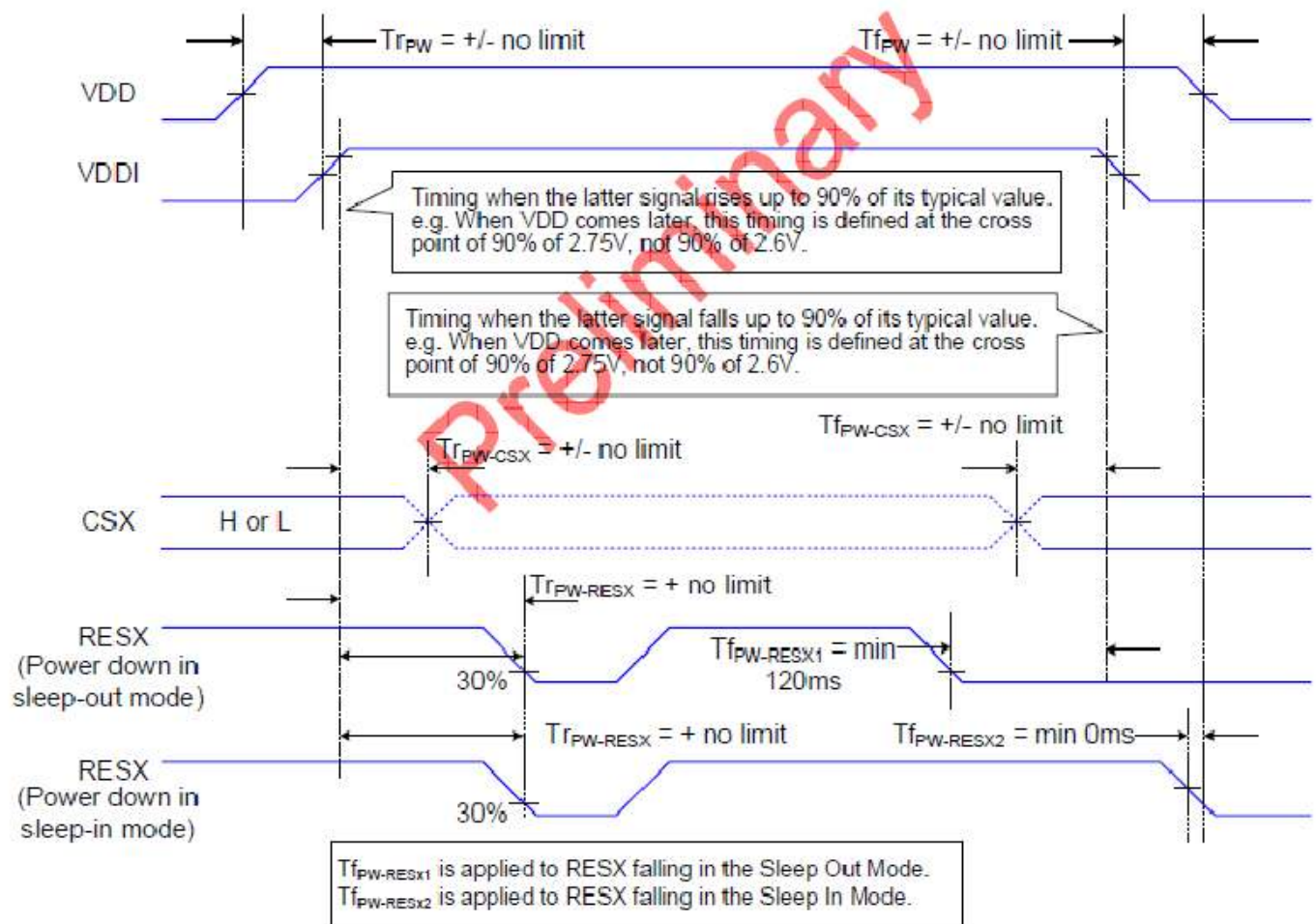
7. Optical Characteristics

Item		Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles		θ T	CR$\geqslant$10	80	85		Degree	Note 2
		θ B		80	85			
		θ L		80	85			
		θ R		80	85			
Contrast Ratio		CR	$\theta = 0°$	700	800			Note 1 Note 3
Response Time		$T_{ON}+T_{OFF}$	25° C		30	40	ms	Note 1 Note 4
Chromaticity	R_x	x		0.640	0.655	0.670		Note 1 Note 5
	R_y	y		0.303	0.318	0.333		
	G_x	x		0.273	0.288	0.303		
	G_y	y		0.530	0.545	0.560		
	B_x	x		0.123	0.138	0.153		
	B_y	y		0.106	0.121	0.136		
Luminance		L		260	400		cd/m^2	Note 1 Note 5

Test Conditions:

1. If=20mA(Backlight current), VDD= 3.0V,the ambient temperature is 25° C.

2. The test systems refer to Note 2.

Note1: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5Minutes operation, the optical properties are measured at the center point of the LCD screen. ALL input terminals LCD panel must be ground when measuring the center area of the panel.
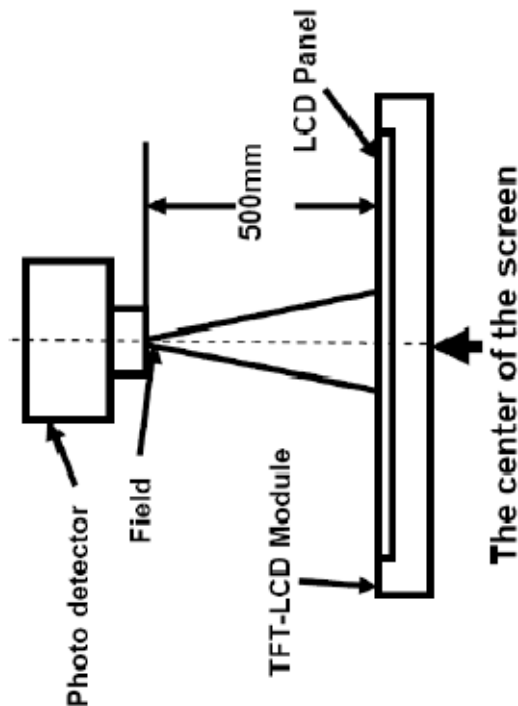
Item	Photo detector	Field
Contrast Ratio	CS1000	1º
Luminance		
Lum Uniformity		
Chromaticity	CS1000	
Response Time	DMS703	
		-

Note2: Definition of viewing angle range and measurement system.
Viewing angle is measured at the center point of the LCD byCONOSCOPE (DMS703)
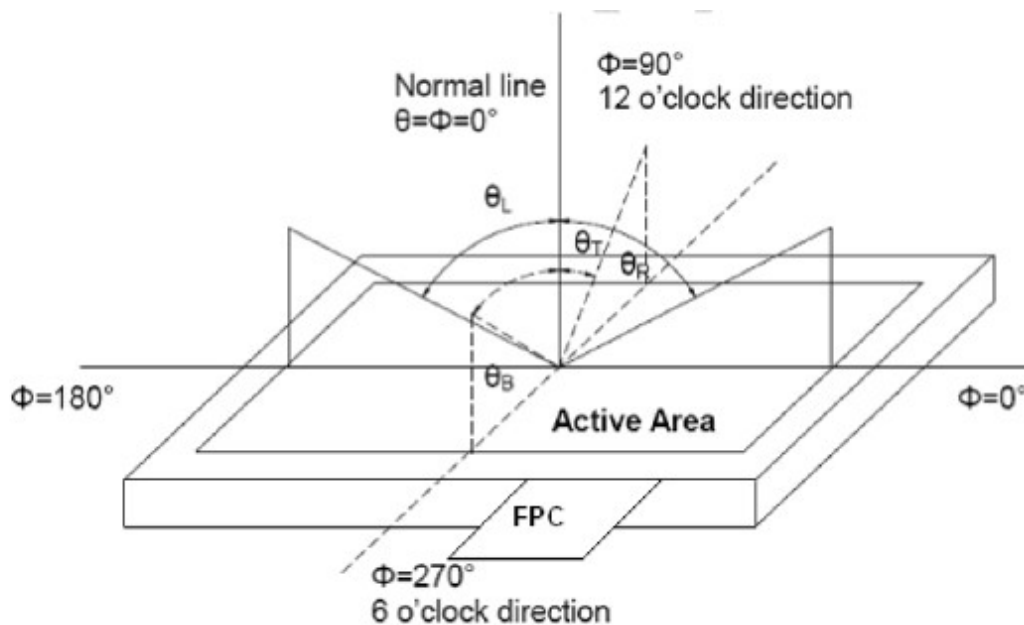
Note3: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

"White state ":The state is that the LCD should drive by Vwhite.

"Black state ":The state is that the LCD should drive by Vblack.
Vwhite: To be determined   Vblack: To be determined
Note4:Definition of Response time
The response time is defined as the LCD optical switching time interval between "White"state and "Black" state. Rise time (T_{ON})is the time between photo detector output intensity changed from 90% to 10%.And fall time (T_{OFF})is the time between photo detector output intensity changed from 10% to90%.
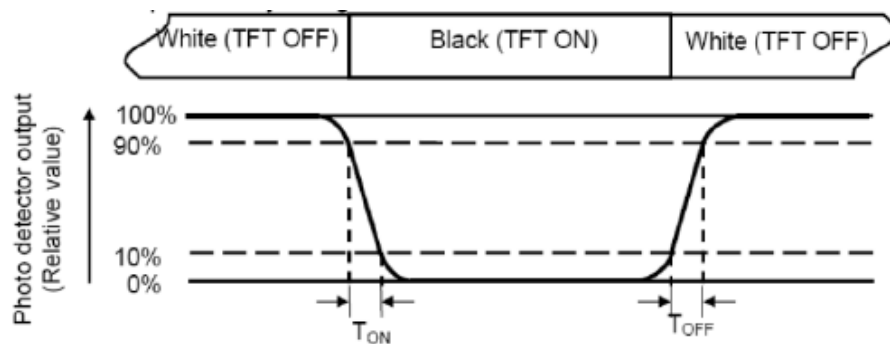
Note5:Definition of color chromaticity（CIE1931）

Color coordinates measured at center point of LCD.
Note6: Definition of Luminance Uniformity
Active area is divided into 9 measuring areas(Refer Fig.2).Every measuring point is placed at the center of each measuring area.
    Luminance Uniformity (U)=Lmin/Lmax
    L--------Active area length   W------- Active area width
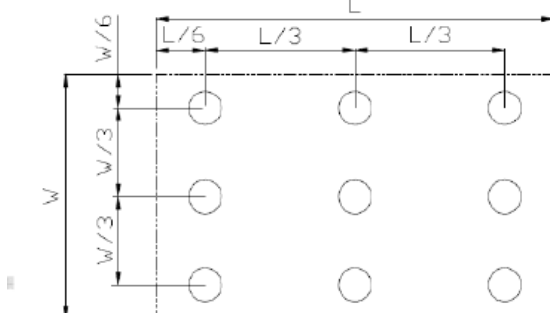
    L max: The measured Maximum luminance of all measurement position.
    L min: The measured Minimum luminance of all measurement position.
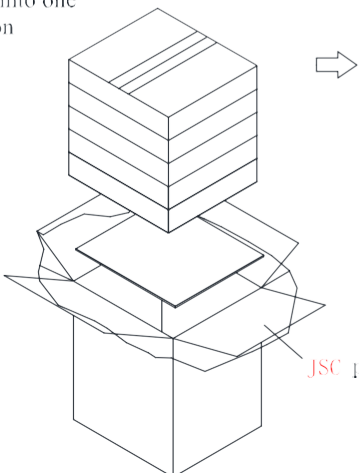Note7: Definition of luminance: Measure the luminance of white state at center point.
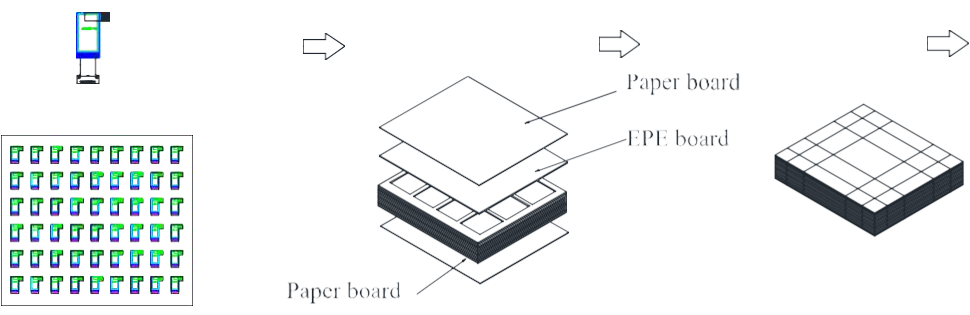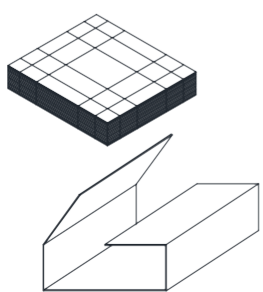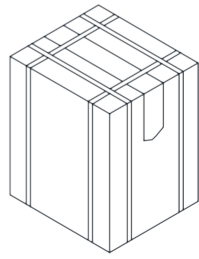
8. Environmental/Reliability Test

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	80±2℃/240hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in theLCD; 2.Sealleak; 3.Non-display; 4.missingsegments; 5.Glasscrack; 6.Current Idd is twice higher than initial value.
2	Low Temperature Storage	-30±2℃/240hours	
3	High Temperature Operating	70±2℃/240hours	
4	Low Temperature Operating	-20±2℃/240hours	
5	Temperature Cycle	-30℃～25℃～80℃ × 10cycles (30min.) (5min.) (30min.)	
6	Damp Proof Test	40℃±5℃×90%RH/240 hours	
7	Vibration Test	Frequency：10Hz~55Hz~10Hz Amplitude：1.5mm, X，Y，Zdirectionfortotal3hours	
8	Dropping test	Drop to the ground from 1m height, one time, every side of carton. (Packing condition)	
9	ESD	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time	
		Voltage:±6KV R: 330Ω C: 150pF Contact discharge, 10time	

Remark:
1. The test samples should be applied to only one test item.
2. Sample size for each test item is 5~10pcs.
3.For Damp Proof Test, Pure water(Resistance＞10MΩ) should be used.
4.In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
5.Failure Judgment   Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.
6.Please use automatic switch menu(or roll menu) testing mode when test operating mode.

9. Packing Drawing

PRODUCT PART NO.: ☐096LAB-02-100N

PACKING TYPE: BY EPE TRAY(T096LAB-02-100A)

PACKLING ORDER:

1) Putting 54 pcs Modules on each EPE tray.

2) Putting 8 pcs EPE trays together with EPE paper on the top of EPE tray.

3) Assembling the boards and the tray together with adhesive tape

Paper board

EPE board

Paper board

4) Putting in the inner small carton (TYPE; II82)

5) Putting 5 small cartons into one outcarton

6) Packing finished

JSC plastic bag

Note: 54 pcs in a tray, 8 trays in a inner carton,5 inner cartons in a out carton, so 54x8x5=2160pcs/Outcartor

Dimension (Small carton): 385*325*87mm   Dimension (Out carton): 394*344*470mm

NO. ☐096LAB-02-100N   Ver.1   Drw: Chk: Apv:

10. Standard Specifications For Product Quality

10.1. Manner of test:

10.1.1 The test must be under 40W fluorescent   light, and the distance of view must   be at 30±10cm.

10.1.2 Room temperature 25±5℃   Humidity: (60±10)％RH.

10.2. Quality specification

It shall   be based   onGB2828-87, inspection levelII .

	IETM	CHE K LEV L	AQL
MAJOR (MA)	1.Liquid crystal leakage 2.Wrong polarizer 3.Outside dimension 4. Bright dot、Dark dot 5. Display abnormal 6. Class crack	II	0.25
MINOR (MI)	1. Spot Defect (Including black spot、white spot、pinhole、foreign particle、bubbles、hurt） 2. fragment 3. Line Defect (Including black line、white line、cratch) 4. Incision defect 5. Newton's ring 6. Other visual defects	II	1.0

10.3.   Definition of area: Ⅰ

10.3.1 Ⅰ area: viewing area

Ⅱ area: outside viewing area
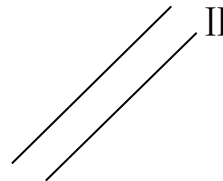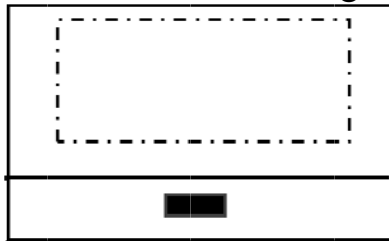
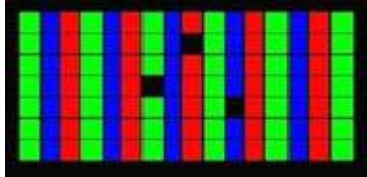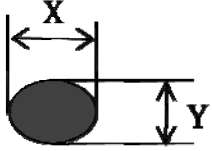## 10.4.Standard of appearance test for Ⅰ area: (unit: mm)
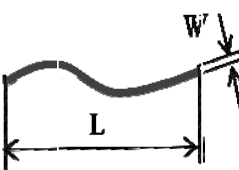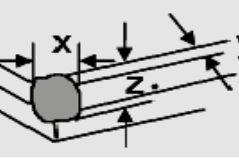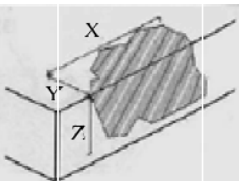
NOTE：Defect ignore for Ⅱ area .
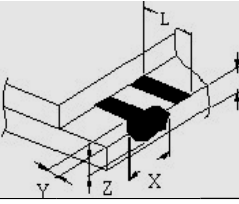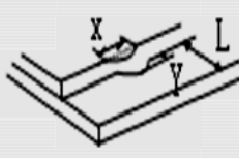
10.4.1 Bright/Dark Dots explain

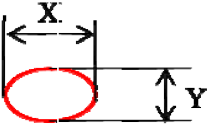| Name	Ex lain	Definition
Bright dot	Dots appear bright and unchanged in size in which LCD panel isdisplaying under black pattern 亮點	The definition of dot: The size of a defective dot over 1/2 of single pixel dot is regarded as one defective dot . NOTE: One pixel consists of 3 sub-pixels, including R,G, and B dot.(Sub-pixel = Dot)
Dark dot	Dots appear dark and unchanged in size in which LCD panel isdisplaying under pure red, green, blue pattern.	
ADJACE NT DOT	Adjacent two sub-pixel are defect（define two dot defect）	

10.4.2 Inspection standard

№	Items	Criterion		Checking Manner	Defect Cl sses
1	Bright/dark dot	Under 6" (contain 6")	Bright dot：2 Dark dot：N≤4 **Note:** be more than 5mm apart	Checking with eyes	MAJ
		6"-12"	Bright dot：N≤4 Dark dot：N≤5 Total Bright and Dark Dots：N≤8 **Note ：** 1. Two bright dot defects (red, green, blue, and white) should be larger than 15mm； 2. The distance between black dot defects or black and bright dot defects should be more than 5mm apart.		
2	Spot Defect (Including black spot. white spot. Pinhole. Foreign particle. bubbles. hurt） X Y D=(X+Y)/2	Under 6" （contain 6"）	D≤0.1 Ignore 0.1 ＜D≤0.35 N≤3 0.35＜D N=0	Checking with eyes	MIN
		6"-12"	D≤0.3 Ignore 0.3 ＜D≤0.6N≤4 0.6＜D N=0		

№	Items	Criterion			Checking manner	Defect classes
3	Line Defect (Including black Line. white line. scratch)	Under 6" （contain 6"）	W≤0.02 Ignore 0.02＜W≤0.04 L≤5 N≤2 0.04＜W≤0.06 L≤5 N≤1 W＞0.06 N=0		Checking with eyes	MIN
		6"-12"	W≤0.07 Ignore 0.07＜W≤0.1 L≤10 N≤4 W＞0.1 N=0			
4	Display abnormal	Not allowed			Checking with eyes	MAJ
5	Outside dimension	Accord with drawing			Callipers	MAJ
6	Class crack	Not allowed			Checking with eyes	MAJ
7	Leak	Not allowed			Checking with eyes	MAJ
8	Comer fragment	X≤3 Y≤3 Z≤T Ignore Note ： 1.No hurt identifying .wire. seal 2.T： Glass thickness X: Length Y: Width Z: thickness			Checking with eyes	MIN
9	Side fragment	Y≤1 Z≤T Ignore Note ： 1.No hurt identifying .wire. seal 2.T：Glass thickness X: Length Y: Width Z: thickness			Checking with eyes	MIN
	Step fragment	Y≤1 and Y≤1/4 L			Checking with eyes	MIN
	Incision defect	Y≤1 and accord with outside dimension			Checking with eyes	MIN

№	Items	Criterion		Checking manner	Defect classes
10	Newton's ring （CTP or Cover board） D=(X+Y)/2	Under 6" (contain 6") 6"-12"	D≤25 N≤3 D＞25 N=0 D≤70 N≤5 D＞70 N=0		Checking with eyes	MIN

11. Precautions for Use of LCD Modules

11.1 Handing Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.

(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.

(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).

(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass,tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.

(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents

    - Isopropyl alcohol

    - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.

    - Water

    - Ketone

    - Aromatic solvents

Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.

(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.

(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I   O cable
or the backlight cable.

(9) Do not attempt to disassemble or process the LCD module.

(10) NC terminal should be open. Do not connect anything.

(11) If the logic circuit power is off, do not apply the input signals.

(12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

- Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.Be sure to ground the body when handling the LCD modules.

- Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.

- To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.

The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

(13)Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

11.2 Storage Precautions
When storing the LCD modules, the following precaution is necessary.
(1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
(2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
(3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

11.3 Others
Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following

sections when handling the modules.
- Exposed area of the printed circuit board.
-Terminal electrode sections.

11.4 USING LCD MODULES
Installing LCD Modules
The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.
(1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.
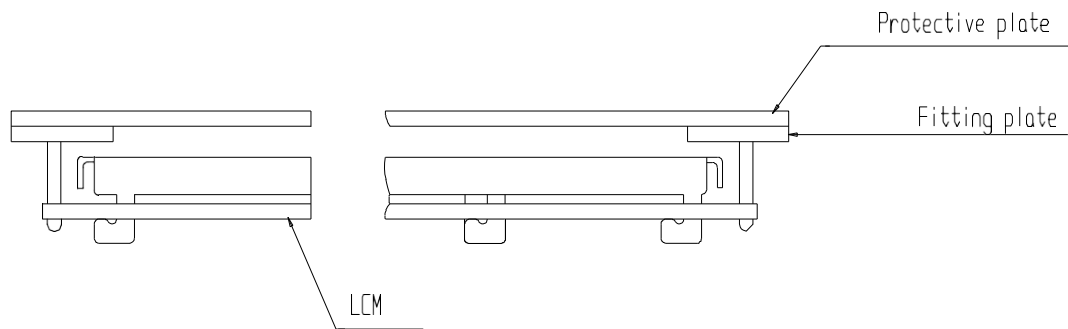
(2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be ±0.1mm.

Precaution for assemble the module with BTB connector:
Please note the position of the male and female connector position,don't assemble or assemble like the method which the following picture shows
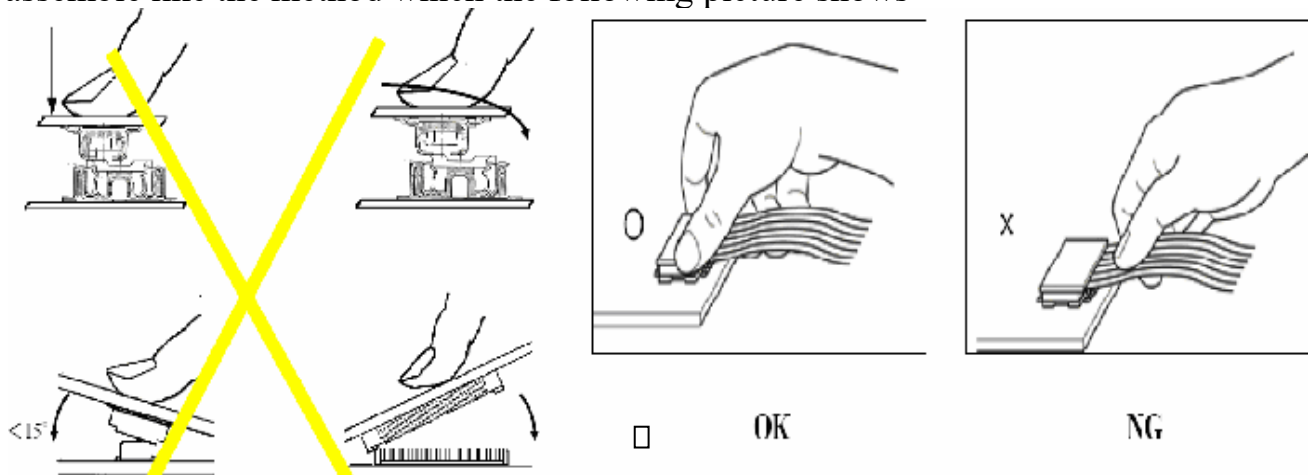
Precaution for soldering to the LCM

	Hand soldering	Machine drag	Machine press soldering
No ROHS Product	290°C~350°C. Time : 3-5S.	330°C ~350°C. Speed : 4-8mm/s.	300°C~330C. Time : 3-6S. Press: 0.8~1.2Mpa
ROHS Product	340°C~370°C. Time : 3-5S.	350°C ~370°C. Time : 4-8 mm/s.	330°C~360C. Time : 3-6S. Press: 0.8~1.2Mpa

(1)If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is

recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.

(2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.

(3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

(1) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life.An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.

(2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, Which will come back in the specified operating temperature.

(3 If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.

(4) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature,50%RH or less is required.

(5) Input each signal after the positive/negative voltage becomes stable.

(6) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

Safety

(1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.

(2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

(3) WEEE order must be executed in product scrap.

11.5The disposal of waste

For waste disposal, our recommendations are as follows, please refer to your company and the relevant provisions of the state laws and regulations of the act accordingly

1. Packing materials disposal for our packaging (carton/PS tray/EPE tray/PET tray)

1)Our company used to recycle and reuse materials, packing materials can be you just need to transfer to material recycling companies

2. Our scrap module can't be recycled for reuse, so please dispose of,

1) Our scrap module can't be recycled for reuse, products and components are "served" can lead to accidents

2) Our scrap can be transfer to material recycling companies, dismantling, to ensure that scrap in relatively advanced technology products, environmental protection measures of relatively perfect environment for processing.

3. WEEE order must be executed in product scrap.

12. Prior Consult Matter

1.①For standard products, we keep the right to change material, process ... for improving the product property without notice on our customer.

②For OEM products, if any change needed which may affect the product property, we will consult with our customer in advance.

2.If you have special requirement about reliability condition, please let us know before you start the test on our samples.

13. Factory

FACTORY NAME: LCD Mikroelektronik GmbH

FACTORY ADDRESS: Otto-Lilienthal-Str. 13, D-76275 Ettlingen