

PRODUCT : LCD MODULE
MODEL NO : LCD240DLBS-01-100N
SUPPLIER : LCD Mikroelektronik GmbH
DATE : May.24.2022

SPECIFICATION

Prepared by	Checked	Approved

CUSTOMER:
MODEL NO.:

DATE:

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CONTENTS

1. General Specifications	4
2. Mechanical Drawing.....	5
3. PIN Assignment	6
4. Absolute Maximum Rating	7
5. Electrical Characteristics	7
5.1. Recommended Operating Condition.....	7
5.2. Recommended Driving Condition for Backlight.....	7
6. Timing Characteristics.....	8
6.1. AC Electrical Characteristics	8
6.2. DC Electrical Characteristics	9
6.3. Timing	10
6.4. Power ON/OFF Sequence.....	11
7. Optical Characteristics	12
8. Environmental/Reliability Test.....	15
9. Packing Drawing	16
10. Standard Specifications For Product Quality	17
10.1. Manner of test	17
10.2. Quality specification	17
10.3. Definition of area	17
10.4. Standard of appearance	18
11. Precautions for Use of LCD Modules	20
11.1. Handling Precautions	20
11.2.....	S
storage Precautions.....	21
11.3. Others	21
11.4. USING LCD MODULES	21
11.5. The disposal of waste.....	23
12. Prior Consult Matter	23
13. Factory.....	23

1. General Specifications

No.	Item	Contents	Unit
1	Size	2.4	inch
2	Resolution	240RGB*320	
3	Interface	16bit parallel (IM[3:0]=0001)	
4	Color Depth	262	K
5	Technology Type	a-Si TFT	
6	Pixel size	0.153*0.153	mm
7	Pixel Arrangement	1P2D	
8	Display Mode	Normally Black, Transmissive, IPS	
9	Viewing Direction	ALL VIEW	
10	LCM (W x H x D)	42.72*60.26*2.15	mm
11	Active Area (W x H)	36.72*48.96	mm
12	With/Without TSP	Without TSP	
13	LED Numbers	4	

2.Mechanical Drawing

<i>CUSTOMER'S APPROVED:</i>	DATE:	PAGE:
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Backlight circuit diagram:
 $V_f = 2.8V(\max.)$ 3.1V(tp) 3.4V(max.)
 $I_f = 80\text{mA}$

1 Operating Voltage:	$V_{CI}=2.75V(\text{typ.})$ 3.3V(max.) $V_{DI}=1.8V$
2 Resolution:	240RGB*320
3 Color:	262K
4 Interface:	16bit parallel (MI[3:0]=6901)
5 Display type:	Transmissive, IPS Normally black
6 Viewing Direction:	ALL VIEW
7 Operating Temp:	-20°C~70°C
8 Storage Temp:	-30°C~80°C
9 Driver IC:	ST7789H2 or equivalent
10 Unspecified tolerance:	±0.3
11 LCM Luminance:	380cd/m ² (mm.) 500cd/m ² (typ.)

TFT PIN.												
1	GND	14	D12	27	/RD							
2	LEDA	15	D11	28	/MR							
3	LEDK4	16	D10	29	/RS							
4	LEDK3	17	D9	30	/CS							
5	LEDK2	18	D8	31	NC							
6	LEDK1	19	D7	32	VDDI							
7	GND	20	D6	33	VCI							
8	NC	21	D5	34	NC							
9	IM0	22	D4	35	NC							
10	RESET	23	D3	36	NC							
11	D15	24	D2	37	NC							
12	D14	25	D1	38	NC							
13	D13	26	D0	39	GND							

NO.	DESCRIPTION	DATE
1	Modify the backlight circuit diagram, backlight module, and luminance.	2022.5.10
2		
3		
4		
5		

Unit: mm

App

Unit: mm

App

Chk

App

3. PIN Assignment

Pin No.	Symbol	I/O	Function	Remark																		
1	GND	P	Ground																			
2	LEDA	P	LED backlight anode.																			
3	LEDK4	P	LED backlight cathode.																			
4	LEDK3	P	LED backlight cathode.																			
5	LEDK2	P	LED backlight cathode.																			
6	LEDK1	P	LED backlight cathode.																			
7	GND	P	Ground																			
8	NC	-	No connected																			
9	IM0	I	The MCU interface mode select.																			
			<table border="1"> <thead> <tr> <th>IM3</th> <th>IM2</th> <th>IM1</th> <th>IM0</th> <th>MPU Interface Mode</th> <th>Data pin</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>0</td> <td>80-8bit parallel I/F</td> <td>DB[7:0]</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>1</td> <td>80-16bit parallel I/F</td> <td>DB[15:0]</td> </tr> </tbody> </table>	IM3	IM2	IM1	IM0	MPU Interface Mode	Data pin	0	0	0	0	80-8bit parallel I/F	DB[7:0]	0	0	0	1	80-16bit parallel I/F	DB[15:0]	
			IM3	IM2	IM1	IM0	MPU Interface Mode	Data pin														
0	0	0	0	80-8bit parallel I/F	DB[7:0]																	
0	0	0	1	80-16bit parallel I/F	DB[15:0]																	
10	RESET	I	This signal will reset the device and it must be applied to properly initialize the chip. Signal is active low.																			
11	D15	I/O	8-bit I/F: DB[7:0] are used. 16-bit I/F: DB[15:0] are used If not used, please fix this pin at VDDI or DGND.																			
12	D14	I/O																				
13	D13	I/O																				
14	D12	I/O																				
15	D11	I/O																				
16	D10	I/O																				
17	D9	I/O																				
18	D8	I/O																				
19	D7	I/O																				
20	D6	I/O																				
21	D5	I/O																				
22	D4	I/O																				
23	D3	I/O																				
24	D2	I/O																				
25	D1	I/O																				
26	D0	I/O																				
27	/RD	I	Read enable in 8080 MCU parallel interface. If not used, please fix this pin at VDDI or DGND.																			
28	/WR	I	Write enable in MCU parallel interface. If not used, please fix this pin at VDDI or DGND.																			
29	RS	I	Display data/command selection pin in parallel interface. This pin is used to be serial interface clock.																			
30	/CS	I	Chip selection pin Low enable. High disable.																			
31	NC	-	No connected																			
32	VDDI	P	Power Supply for I/O System																			
33	VCI	P	Power Supply for Analog, Digital System and Booster Circuit.																			
34	NC	-	No connected																			
35	NC	-	No connected																			
36	NC	-	No connected																			
37	NC	-	No connected																			
38	NC	-	No connected																			
39	GND	P	Ground																			

PS. For further details, please refer to ST7789H2 data sheet.

4. Absolute Maximum Rating

AGND = GND = 0V, Ta = 25°C

Item	Symbol	Min	Max	Unit	Remark
Power Voltage	VCI	-0.3	4.6	V	
	VDDI	-0.3	4.6		
Operating Temperature	T _{OPR}	-20	70	°C	
Storage Temperature	T _{STG}	-30	80	°C	

Note1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

5. Electrical Characteristics

5.1. Recommended Operating Condition

AGND = GND = 0V, Ta = 25°C

Item	Symbol	Min	Typ.	Max	Unit	Remark
Power Voltage	VCI	2.5	2.75	3.3	V	
	VDDI	1.65	1.8	3.3	V	
Input logic high voltage	V _{ih}	0.7 VDDI	-	VDDI	V	
Input logic low voltage	V _{il}	0	-	0.3VDDI	V	

5.2. Recommended Driving Condition for Backlight

Ta = 25°C

Item	Symbol	Min	Typ.	Max	Unit	Remark
Forward Voltage	V _f	2.8	3.1	3.4	V	
Forward Current	I _f		80		mA	
Operating Life Time	-	30000			Hours	

Note 1: Ta means ambient temperature of TFT-LCD module.

Note 2: If the module is driven by high current or at high ambient temperature & humidity condition. The operating life will be reduced.

Note 3: Operating life means brightness goes down to 50% initial brightness. Minimum operating life time is estimated data.

6. Timing Characteristics

6.1. AC Electrical Characteristics

VDDI=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta= -30 to 70 °C

Signal	Symbol	Parameter	Min	Max	Unit	Description
D/CX	T _{AST}	Address setup time	0		ns	-
	T _{AHT}	Address hold time (Write/Read)	10		ns	
CSX	T _{CHW}	Chip select "H" pulse width	0		ns	-
	T _{CS}	Chip select setup time (Write)	15		ns	
	T _{RCS}	Chip select setup time (Read ID)	45		ns	
	T _{RCSFM}	Chip select setup time (Read FM)	355		ns	
	T _{CSF}	Chip select wait time (Write/Read)	10		ns	
	T _{CSH}	Chip select hold time	10		ns	
WRX	T _{WC}	Write cycle	66		ns	
	T _{WRH}	Control pulse "H" duration	15		ns	
	T _{WRL}	Control pulse "L" duration	15		ns	
RDX (ID)	T _{RC}	Read cycle (ID)	160		ns	When read ID data
	T _{RDH}	Control pulse "H" duration (ID)	90		ns	
	T _{RDL}	Control pulse "L" duration (ID)	45		ns	
RDX (FM)	T _{RCFM}	Read cycle (FM)	450		ns	When read from frame memory
	T _{RDHFM}	Control pulse "H" duration (FM)	90		ns	
	T _{RDLFM}	Control pulse "L" duration (FM)	355		ns	
D[17:0]	T _{DST}	Data setup time	10		ns	For CL=30pF
	T _{DHT}	Data hold time	10		ns	
	T _{RAT}	Read access time (ID)		40	ns	
	T _{RATFM}	Read access time (FM)		340	ns	
	T _{ODH}	Output disable time	20	80	ns	

8080 Parallel Interface Characteristics

6.2. DC Electrical Characteristics

Parameter	Symbol	Condition	Specification			Unit	Related Pins
			MIN.	TYP.	MAX.		
Power & Operation Voltage							
System Voltage	VCI	Operating voltage	2.4	2.75	3.3	V	
Interface Operation Voltage	VDDI	I/O Supply Voltage	1.65	1.8	3.3	V	
Gate Driver High Voltage	VGH		12.2		14.97	V	Note 4
Gate Driver Low Voltage	VGL		-12.5		-7.16	V	
Gate Driver Supply Voltage		VGH-VGL	19.36		27.47	V	Note 5
Input / Output							
Logic-High Input Voltage	VIH		0.7VDDI		VDDI	V	Note 1
Logic-Low Input Voltage	VIL		VSS		0.3VDDI	V	Note 1
Logic-High Output Voltage	VOH	IOH = -1.0mA	0.8VDDI		VDDI	V	Note 1
Logic-Low Output Voltage	VOL	IOL = +1.0mA	VSS		0.2VDDI	V	Note 1
Logic-High Input Current	I _{IH}	V _{IN} = VDDI			1	uA	Note 1
Logic-Low Input Current	I _{IL}	V _{IN} = VSS	-1			uA	Note 1
Input Leakage Current	I _{IL}	IOH = -1.0mA	-0.1		+0.1	uA	Note 1
VCOM Voltage							
VCOM amplitude	VCOM			VSS		V	
Source Driver							
Source Output Range	V _{sout}		V _{AN}		V _{AP}	V	
Gamma Reference Voltage(Positive)	V _{AP}		4.45		6.4	V	Note 6
Gamma Reference Voltage(Negative)	V _{AN}		-4.6		-2.65	V	
Source Output Settling Time	Tr	Below with 99% precision			20	us	Note 2
Output Offset Voltage	V _{OFFSET}				35	mV	Note 3

Basic DC Characteristics

Note 1: TA= -30 to 70°C (to +85°C no damage).

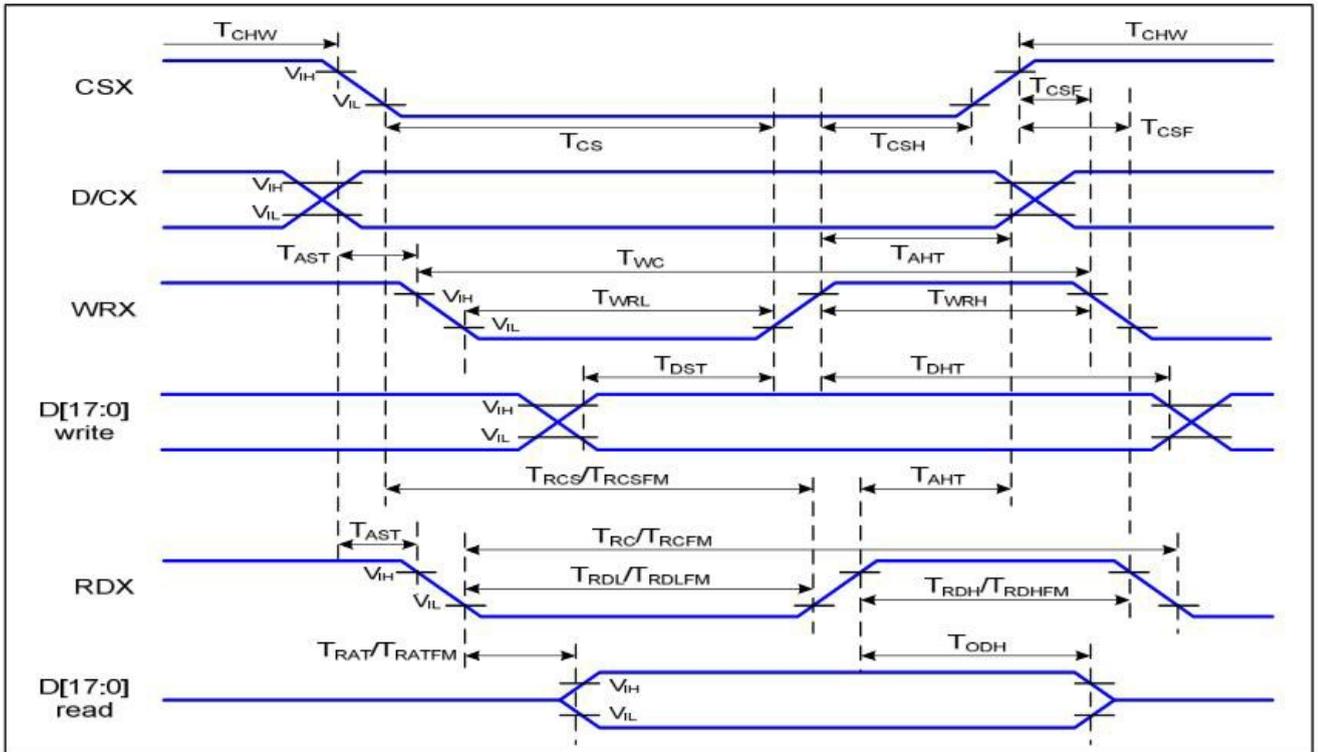
Note 2: Source channel loading= 2KΩ+12pF/channel, Gate channel loading=5KΩ+40pF/channel.

Note 3: The Max. value is between measured point of source output and gamma setting value.

Note 4: When evaluating the maximum and minimum of VGH, VCI=2.8V.

6. 3. Timing

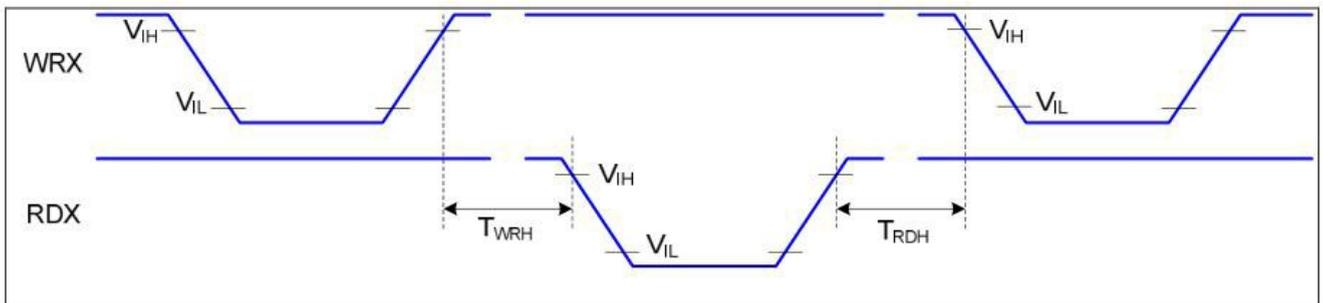
8080 Series MCU Parallel Interface Characteristics: 18/16/9/8-bit Bus



Parallel Interface Timing Characteristics (8080-Series MCU Interface)



Rising and Falling Timing for I/O Signal



Write-to-Read and Read-to-Write Timing

6. 4. Power ON/OFF Sequence

VDDI and VCI can be applied in any order.

VCI and VDDI can be power down in any order.

During power off, if LCD is in the Sleep Out mode, VCI and VDDI must be powered down minimum 120msec after RESX has been released.

During power off, if LCD is in the Sleep In mode, VDDI or VCI can be powered down minimum 0msec after RESX has been released.

CSX can be applied at any timing or can be permanently grounded. RESX has priority over CSX.

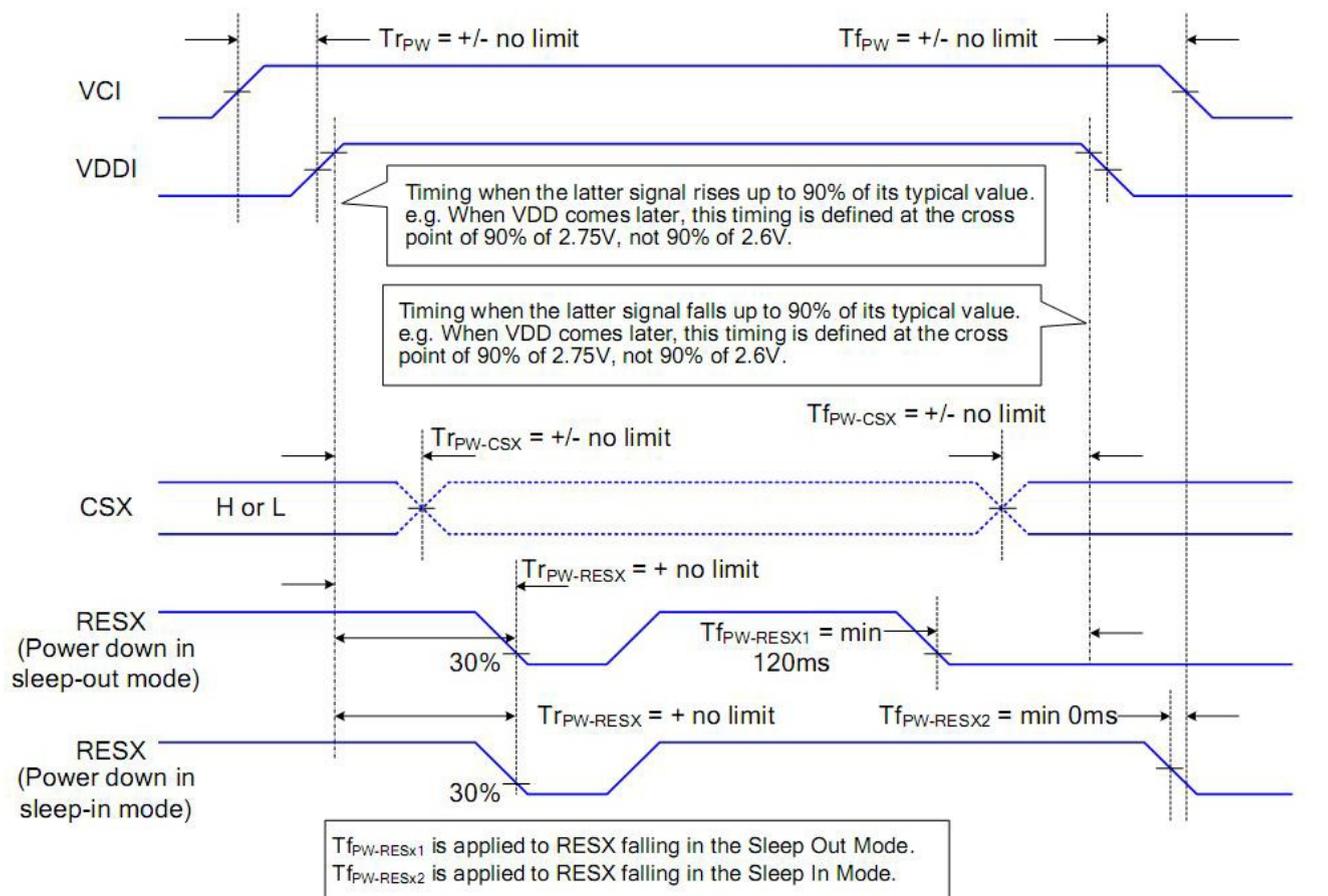
Note 1: There will be no damage to the display module if the power sequences are not met.

Note 2: There will be no abnormal visible effects on the display panel during the Power On/Off Sequences.

Note 3: There will be no abnormal visible effects on the display between end of Power On Sequence and before receiving Sleep Out command. Also between receiving Sleep In command and Power Off Sequence.

Note 4: If RESX line is not held stable by host during Power On Sequence as defined in the sequence below, then it will be necessary to apply a Hardware Reset (RESX) after Host Power On Sequence is complete to ensure correct operation. Otherwise function is not guaranteed.

The power on/off sequence is illustrated below



7. Optical Characteristics

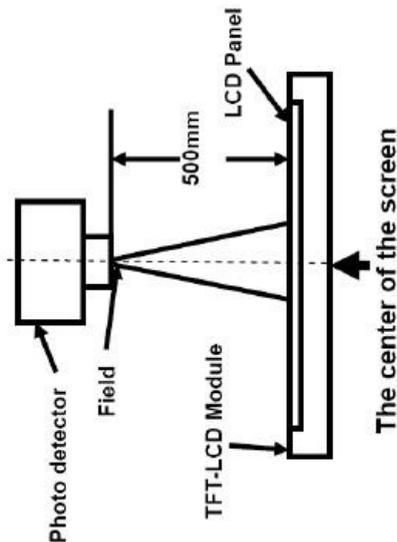
Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
View Angles	θT	$CR \geq 10$	70	80	-	Degree	Note 2
	θB		70	80	-		
	θL		70	80	-		
	θR		70	80	-		
Contrast Ratio	CR	$\theta = 0^\circ$	(1000)	(1500)	-		Note 1 Note 3
Response Time	$T_{ON}+T_{OFF}$	25°C	-	35	45	ms	Note 1 Note 4
Chromaticity	W_x	x	(0.267)	(0.307)	(0.347)		Note 1 Note 5
	W_y	y	(0.294)	(0.334)	(0.374)		
	R_x	x	(0.612)	(0.652)	(0.692)		
	R_y	y	(0.282)	(0.322)	(0.362)		
	G_x	x	(0.236)	(0.276)	(0.316)		
	G_y	y	(0.542)	(0.582)	(0.622)		
	B_x	x	(0.101)	(0.141)	(0.181)		
	B_y	y	(0.051)	(0.091)	(0.131)		
Uniformity	U		75	-	-	%	Note 5
Luminance	L		380	500	-	cd/m ²	Note 1 Note 5

Test Conditions:

1. $I_f=80mA$ (Backlight current), $V_{CI}=2.75V$, the ambient temperature is 25°C.
2. The test systems refer to Note 2.

Note1: Definition of optical measurement system.

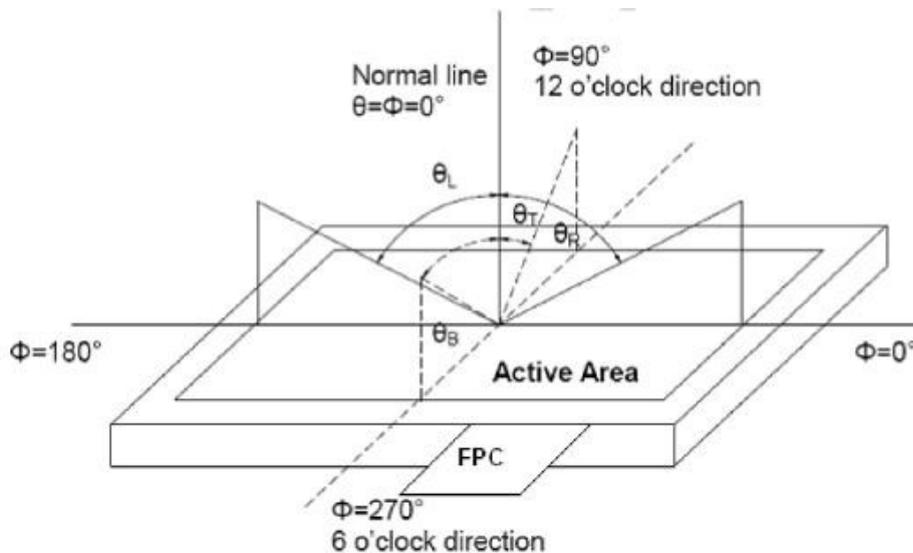
The optical characteristics should be measured in dark room. After 5Minutes operation, the optical properties are measured at the center point of the LCD screen. ALL input terminals LCD panel must be ground when measuring the center area of the panel.



Item	Photo detector	Field
Contrast Ratio	CS1000	1°
Luminance		
Lum Uniformity		
Chromaticity	CS1000	
Response Time	DMS703	-

Note2: Definition of viewing angle range and measurement system.

Viewing angle is measured at the center point of the LCD by CONOSCOPE (DMS703)



Note3: Definition of contrast ratio

White state :The state is that the LCD should drive by Vwhite.

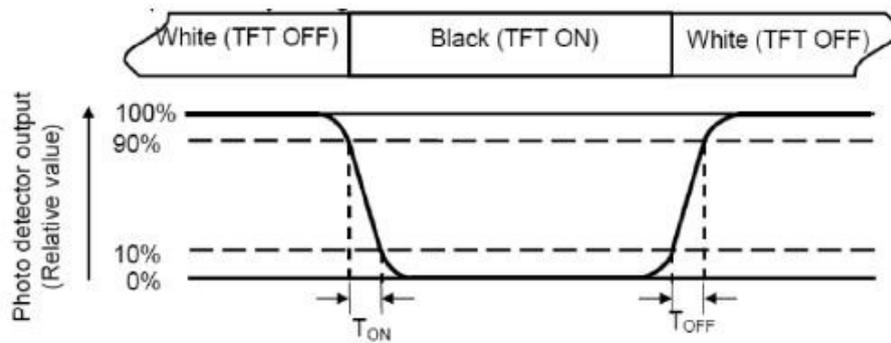
$$\text{Contrast ratio(CR)} = \frac{\text{Luminance measured when LCD is on the "White" state}}{\text{Luminance measured when LCD is on the "Black" state}}$$

"Black state":The state is that the LCD should drive by Vblack.

Vwhite: To be determined Vblack: To be determined

Note4: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White"state and "Black" state. Rise time (TON)is the time between photo detector output intensity changed from 90% to 10%.And fall time (TOFF)is the time between photo detector output intensity changed from 10% to90%.



Note5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Color coordinates are subject to actual measurement.

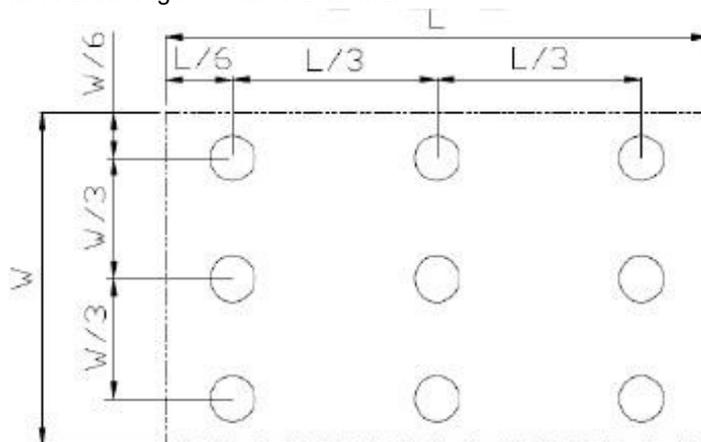
If the data has a bracket, that means reference value of TFT panel or one sample of module, the values of module TBD.

Note6: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas(Refer Fig.2).Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (U)= L_{min}/L_{max}

L-Active area length W-Active area width



L_{max} : The measured Maximum luminance of all measurement position.

L_{min} : The measured Minimum luminance of all measurement position.

Note7: Definition of luminance: Measure the luminance of white state at center point.

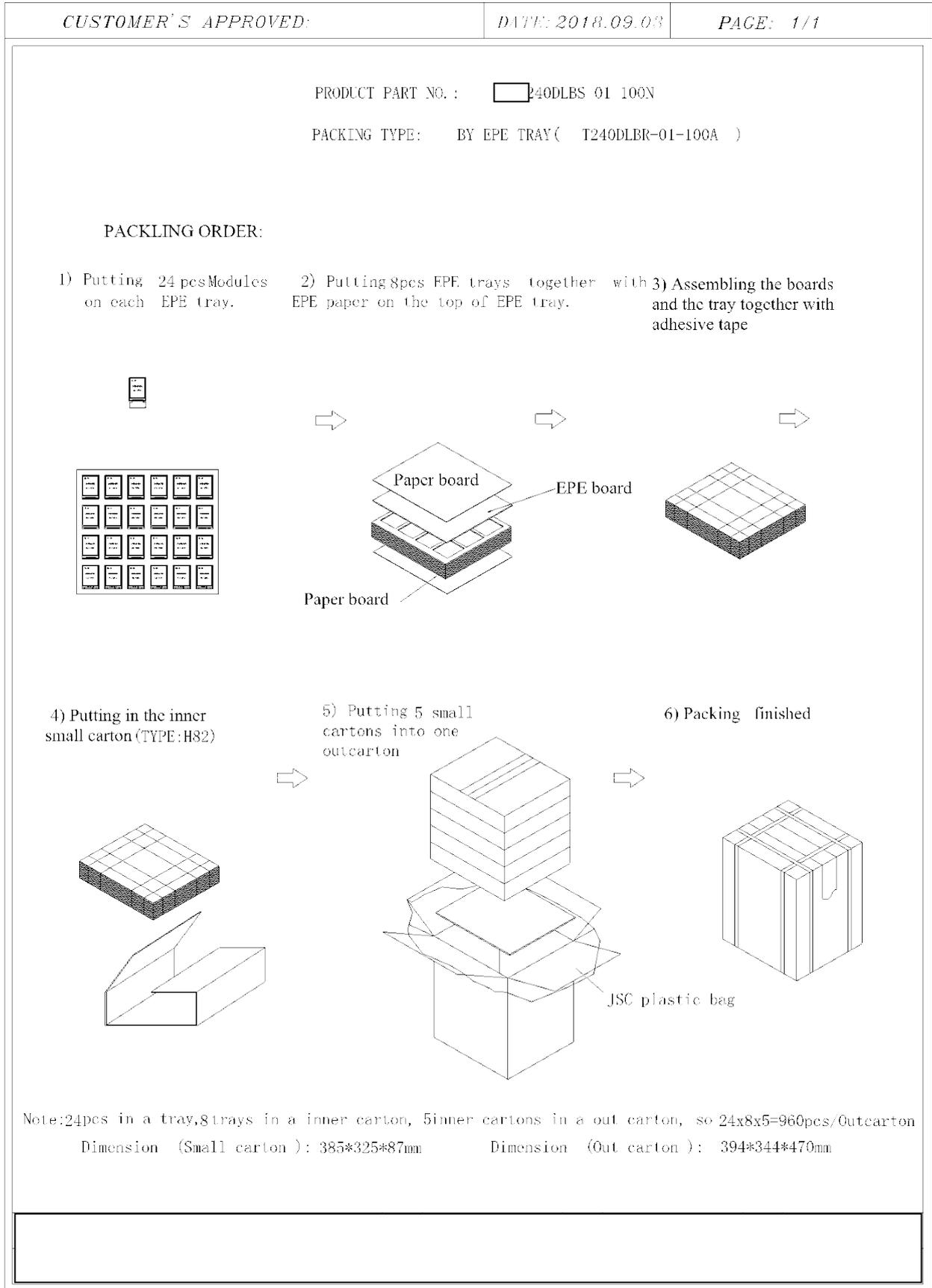
8. Environmental/Reliability Test

No.	Test Item	Test Condition	Inspection after test
1	High Temperature Storage	80±2°C/240 hours	Inspection after 2~4hours storage at room temperature, the sample shall be free from defects: 1.Air bubble in the LCD; 2.Sealleak; 3.Non-display; 4.Missing segments; 5.Glass crack; 6.Current Idd is twice higher than initial value.
2	Low Temperature Storage	-30±2°C/240 hours	
3	High Temperature Operating	70±2°C/240 hours	
4	Low Temperature Operating	-20±2°C/240 hours	
5	Temperature Cycle	-30°C~ 25°C~ 80°C × 10cycles (30min.) (5min.) (30min.)	
6	Damp Proof Test	40°C±5°C×90%RH/240 hours	
7	Vibration Test	Frequency: 10Hz~55Hz~10Hz Amplitude: 1.5mm, X, Y, Z direction for total 3hours (Packing condition)	
8	Dropping test	Drop to the ground from 1m height, one time,every side of carton. (Packing condition)	
9	ESD test	Voltage:±8KV R: 330Ω C: 150pF Air discharge, 10time Voltage:±6KV R: 330Ω C: 150pF Contact discharge, 10time	

Remark:

1. The test samples should be applied to only one test item.
2. Sample size for each test item is 5~10pcs.
3. For Damp Proof Test, Pure water(Resistance> 10MΩ) should be used.
4. In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judge as a good part.
5. Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.
6. Please use automatic switch menu(or roll menu) testing mode when test operating mode.

9. Packing Drawing



10. Standard Specifications For Product Quality

10.1. Manner of test:

10.1.1 The test must be under 40W fluorescent light, and the distance of view must be at 35±5cm

10.1.2 Room temperature 25±5°C Humidity: (65±5)%RH.

10.1.3 If the product is uneven and bright spot, use 2%ND filter to check and confirm. Not visible, OK.

10.1.4 Inspection Angle:

The vision of inspector should be perpendicular to the surface of the Module.

10.1.5 Inspection time:

Perceptibility Test Time: 20 seconds max.

10.2. Quality specification

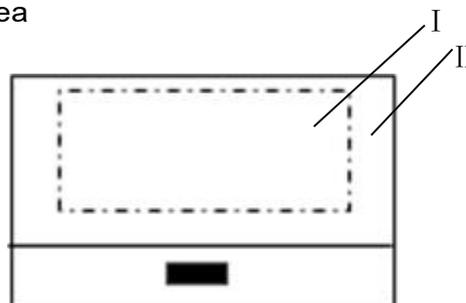
It shall be based on GB2828, inspection level II.

	IETM	CHECK LEVEL	AQL
MAJOR (MA)	1. Liquid crystal leakage 2. Wrong polarizer 3. Outside dimension 4. Bright dot, Dark dot 5. Display abnormal 6. Glass crack	II	0.65
MINOR (MI)	1. Spot Defect (Including black spot, white spot, pinhole, foreign particle, bubbles, hurt) 2. Fragment 3. Line Defect (Including black line, white line, scratch) 4. Incision defect 5. Newton's ring 6. Other visual defects	II	1.0

10.3 Definition of area

10.3.1 I area: viewing area

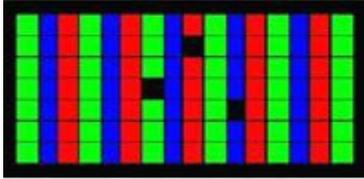
“ area: outside viewing area



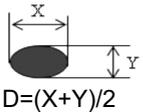
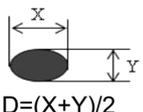
10.4. Standard of appearance test for I area: (unit: mm)

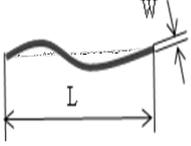
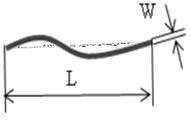
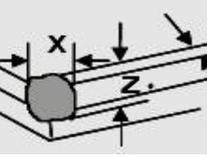
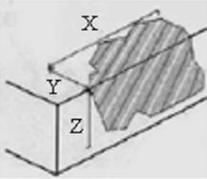
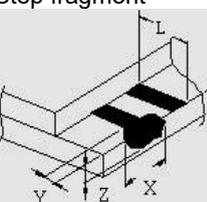
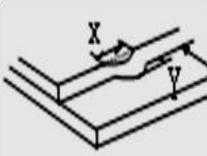
NOTE: Defect ignore for II area.

10.4.1 Bright/Dark Dots explain

Name	Explain	Definition
Bright dot	Dots appear bright and unchanged in size in which LCD panel is displaying under black pattern. 	The definition of dot: The size of a defective dot over 1/2 of single pixel dot is regarded as one defective dot . Note:One pixel consists of 3 sub-pixels, including R,G, and B dot.(Sub-pixel = Dot)
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue pattern. 	
Adjacent Dot	Adjacent two sub-pixel are defect (define two dot defect)	

10.4.2 Inspection standard

No	Items	Criterion				Checking manner	Defect classes
1	Bright/dark dot (亮、暗点)	LCD≤4.3"	4.3"<LCD<7"	7"≤LCD≤12"	LCD>12"	Checking with eyes	MAJ
		Bright dot: N≤2	Bright dot: N≤3	Bright dot: N≤4	Bright dot: N≤5		
		Dark dot: N≤3	Dark dot: N≤4	Dark dot: N≤5	Dark dot: N≤6		
		Total: N≤4	Total: N≤6	Total: N≤8	Total: N≤10		
The distance between the two defect dots shall be greater than 5mm The distance between two defect dots above 7 inches shall be more than 10 mm							
Note: Adjacent dot defect N≤0							
2	Spot defects (black and white spot, pinhole, foreign matter, dent, backlight foreign matter)  $D=(X+Y)/2$	D≤0.15 Ignore 0.15 <D≤0.3 N≤3 0.3<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤4 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤5 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤6 0.5<D N=0	Checking with eyes	MIN
		D≤0.2 Ignore 0.2 <D≤0.5 N≤3 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤4 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤5 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤6 0.5<D N=0		
3	Bubble  $D=(X+Y)/2$	D≤0.2 Ignore 0.2 <D≤0.5 N≤3 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤4 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤5 0.5<D N=0	D≤0.2 Ignore 0.2 <D≤0.5 N≤6 0.5<D N=0		

No	Items	Criterion				Checking manner	Defect classes
		LCD≤4.3"	4.3"<LCD<7"	7"≤LCD≤12"	LCD>12"		
4	Line defects(black and white line, backlight foreign matter etc.) 	$W \leq 0.03$ Ignore 0.03< $W \leq 0.06$ $L \leq 5 \quad N \leq 3$ $W > 0.06$ $L > 5 \quad N = 0$	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.1$ $L \leq 5 \quad N \leq 4$ $W > 0.1 \quad L$ $> 5 \quad N = 0$	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.1$ $L \leq 5 \quad N \leq 5$ $W > 0.1 \quad L$ $> 5 \quad N = 0$	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.1$ $L \leq 5 \quad N \leq 6$ $W > 0.1 \quad L$ $> 5 \quad N = 0$	Checking with eyes	MIN
5	Scratch 	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.2$ $1.0 < L \leq 5.0$ $N \leq 3$ $W > 0.2$ $L > 5 \quad N = 0$	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.2$ $1.0 < L \leq 5.0$ $N \leq 4$ $W > 0.2$ $L > 5 \quad N = 0$	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.2$ $1.0 < L \leq 5.0$ $N \leq 5$ $W > 0.2$ $L > 5 \quad N = 0$	$W \leq 0.03$ Ignore 0.03 $< W \leq 0.2$ $1.0 < L \leq 5.0$ $N \leq 6$ $W > 0.2$ $L > 5 \quad N = 0$	Checking with eyes	MIN
6	Display abnormal	Not allowed				Checking with eyes	MAJ
7	Outside dimension	Accord with drawing				Calipers	MAJ
8	Glass crack	Not allowed				Checking with eyes	MAJ
9	Leak	Not allowed				Checking with eyes	MAJ
10	Comer fragment 	$X \leq 3 \quad Y \leq 3 \quad Z \leq T$ Ignore Note : 1、no hurt identifying、wire、seal 2、T: Glass thickness X: Length Y: Width Z: thickness				Checking with eyes	MIN
11	Side fragment 	$Y \leq 1 \quad Z \leq T$ Ignore Note : 1、no hurt identifying、wire、seal 2、T: Glass thickness X: Length Y: Width Z: thickness				Checking with eyes	MIN
	Step fragment 	$Y \leq 1$ and $Y \leq 1/4 L$				Checking with eyes	MIN
	Incision defect 	$Y \leq 1$ and accord with outside dimension				Checking with eyes	MIN

11. Precautions for Use of LCD Modules

11.1 Handling Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizer with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming in contact with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol
 - Do not scrub hard to avoid damaging the display surface.
- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solventsWipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Electro-Static Discharge Control, Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential. Be sure to ground the body when handling the LCD modules.
 - Tools required for assembling, such as soldering irons, must be properly grounded. make certain the AC power source for the soldering iron does not leak. When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions. To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended. As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.The LCD module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

- (13) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
 - Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
 - Do not drop, bend or twist LCM.

11.2 Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

11.3 Others

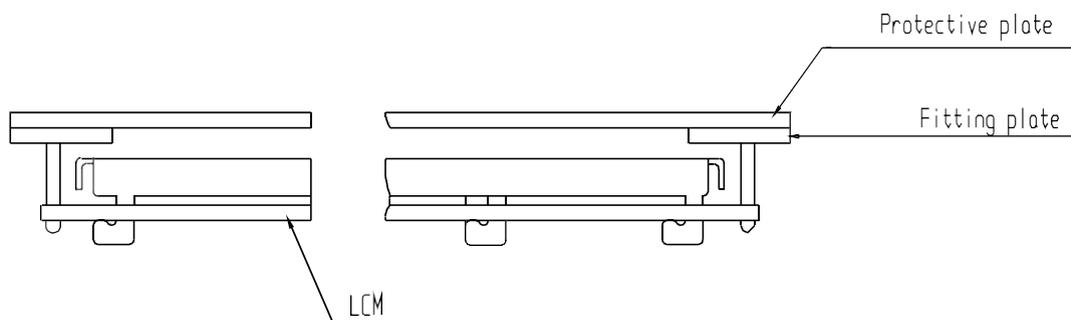
- (1) Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
- (2) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- (3) To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
 - Exposed area of the printed circuit board.
 - Terminal electrode sections.

11.4 USING LCD MODULES

Installing LCD Modules

The hole in the printed circuit board is used to fix LCM as shown in the picture below. Attend to the following items when installing the LCM.

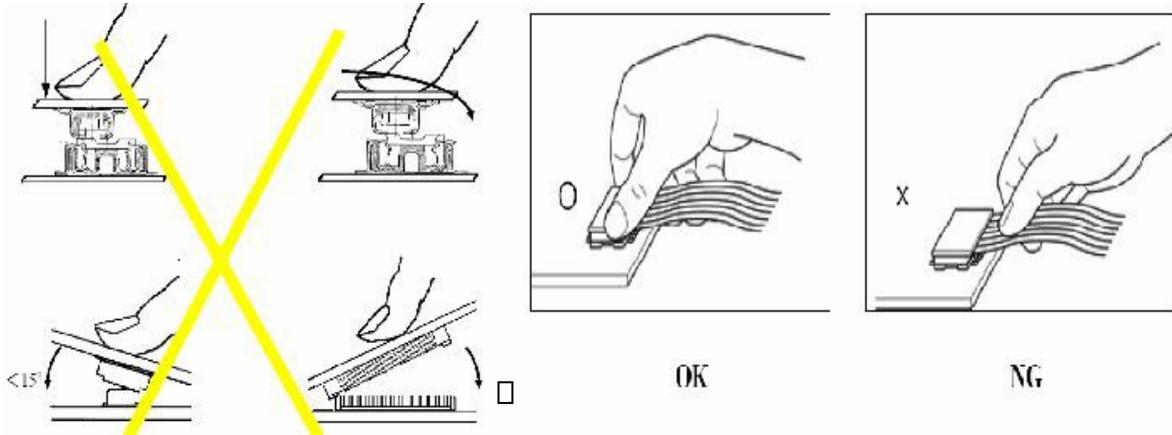
- (1) Cover the surface with a transparent protective plate to protect the polarizer and LC cell.



- (2) When assembling the LCM into other equipment, the spacer to the bit between the LCM and the fitting plate should have enough height to avoid causing stress to the module surface, refer to the individual specifications for measurements. The measurement tolerance should be 0.1mm.

Precaution for assemble the module with BTB connector:

Please note the position of the male and female connector position, don't assemble or assemble like the method which the following picture shows



Precaution for soldering to the LCM

	Hand soldering	Machine drag soldering	Machine press soldering
No ROHS Product	290 C~350 C. Time :3-5S.	330 C ~350 C. Speed : 4-8mm/s.	300 C~330C. Time : 3-6S. Press: 0.8~1.2Mpa
ROHS Product	340 C~370 C. Time:3-5S.	350 C ~370 C. Time : 4-8 mm/s.	330 C~360C. Time : 3-6S. Press: 0.8~1.2Mpa

- (1) If soldering flux is used, be sure to remove any remaining flux after finishing to soldering operation. (This does not apply in the case of a non-halogen type of flux.) It is recommended that you protect the LCD surface with a cover during soldering to prevent any damage due to flux spatters.
- (2) When soldering the electroluminescent panel and PC board, the panel and board should not be detached more than three times. This maximum number is determined by the temperature and time conditions mentioned above, though there may be some variance depending on the temperature of the soldering iron.
- (3) When remove the electroluminescent panel from the PC board, be sure the solder has completely melted, the soldered pad on the PC board could be damaged.

Precautions for Operation

- (1) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage then the limit cause the shorter LCD life. An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current drive should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's, which will come back in the specified operating temperature.
- (3) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (4) A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit. Usage under the maximum operating temperature, 50%RH or less is required.
- (5) Input each signal after the positive/negative voltage becomes stable.
- (6) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.

Safety

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leaks out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

11.5 The disposal of waste

For waste disposal, our recommendations are as follows, please refer to your company, and the relevant provisions of the state laws and regulations of the act accordingly

1. Packing materials disposal for our packaging (carton/PS tray/EPE tray/PET tray)

- 1) Our company used to recycle and reuse materials, packing materials can be you just need to transfer to material recycling companies
2. Our scrap module can't be recycled for reuse, so please dispose of:
 - 1) Our scrap module can't be recycled for reuse, products and components are "served" can lead to accidents
 - 2) Our scrap can be transfer to material recycling companies, dismantling, to ensure that scrap in relatively advanced technology products, environmental protection measures of relatively perfect environment for processing.
3. WEEE order must be executed in product scrap.

12. Prior Consult Matter

1. (1) For standard products, we keep the right to change material, process...for improving the product property without notice on our customer.
(2) For OEM products, if any change needed which may affect the product property, we will consult with our customer in advance.
2. If you have special requirement about reliability condition, please let us know before you start the test on our samples.

13. Factory

FACTORY NAME: LCD Mikroelektronik GmbH

FACTORY ADDRESS: Otto-Lilienthal-Str. 13, D-76275 Ettlingen